

JUN 09 2005

**COHEN, PONTANI, LIEBERMAN & PAVANE**

551 Fifth Avenue

New York, New York

10176-1022

**TELEFAX TRANSMITTAL**

June 9, 2005

To: Examiner Baoquoc N. TO  
U.S. Patent and Trademark Office  
Phone No. 571-272-4041

Fax#: 703-872-9306

From: Michael C. Stuart, Esq.  
Phone No.: 212-687-2770  
Fax No.: 212-972-5487  
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Re: U.S. Patent Appln. No. 09/969,846  
Our File: 4925-122

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- ☐ Confirmation will follow  
☒ Confirmation will not follow

**AFTER FINAL COMMUNICATION**

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**NOTICE OF APPEAL FROM THE PRIMARY EXAMINER  
TO THE BOARD OF PATENT APPEALS AND INTERFERENCES**

Attorney Docket No. TI-32287

In re application of:

Kevin K. Main, et al.

Serial No.: 09/990,854

For: Low Pin Count (LPC) I/O Bridge

Filed: November 9, 2001

Conf. No.: 2733

Group Art Unit: 2111

To: Commissioner for Patents

Sir:

Applicant hereby appeals to the Board of Patent Appeals and Interferences from the decision in the Office Action dated March 9, 2005 and the Advisory Action dated May 24, 2005 of the Primary Examiner finally rejecting Claims 1-14 and 19-27. The item(s) checked below are appropriate:

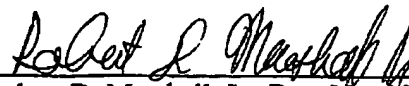
1. ☐ An extension of time to respond to the final rejection  
☐ was granted on  
☐ is requested for ☐ month - see attached copy.
2. ☒ A timely response to the final rejection has been filed, as provided in 841 O.G. 1411.
3. ☒ Fee \$500.00:  
☐ Not required (*Fee paid in prior appeal*)  
☒ The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Texas Instruments Incorporated, Deposit Account No. 20-0668.

06/10/2005 MBIZUNES 00000038 200668 09990854  
01 FC:1401 500.00 DA

Signature [Rule 191(b)]

Post Office Address

(to which correspondence is to be sent)

  
Robert D. Marshall, Jr., Reg. No. 28,527  
Texas Instruments Incorporated  
P.O. Box 655474, MS 3999  
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To: Technology Center: 2111  
Facsimile Number: 703-872-9306

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From: Robert D. Marshall, Jr.  
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JUN 09 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-32287

Kevin K. Main, et al.

Art Unit: 2111

Serial No.: 09/990,854

Examiner: Christopher A. Daley


Filed: November 9, 2001

Conf. No.: 2733

For: Low Pin Count (LPC) I/O Bridge

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9306 on the date shown below:

  
Robin E. Barnum

June 9, 2005  
Date

FACSIMILE COVER SHEET

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<input type="checkbox"/> CONTINUATION APP'N (# Pages)	
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Kevin K. Main, et al.	
TITLE OF INVENTION: Low Pin Count (LPC) I/O Bridge	
TI FILE NO.: <b>TI-32287</b>	DEPOSIT ACCT. NO.: 20-0668
FAXED: 6/9/05 DUE: 6/9/05 ATTY/SECY: RDM/reb	RECEIPT DATE & SERIAL NO.: Serial No.: <b>09/990,854</b> Filing Date: November 9, 2001

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